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| Notice of References Cited | Application/Control No. 10/651,521 | Applicant(s)/Patent Under Reexamination MAIN ET AL. | |
| | Examiner Jeremy S. Cerullo | Art Unit 2112 | Page 1 of 1 |

U.S. PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Name | Classification |
|---|---|--|-----------------|---------------------|----------------|
| | A | US-5,590,377 A | 12-1996 | Smith, Michael G. | 710/303 |
| | B | US-6,336,158 B1 | 01-2002 | Martwick, Andrew W. | 710/104 |
| | C | US-2003/0093607 A1 | 05-2003 | Main et al. | 710/306 |
| | D | US-2003/0221036 A1 | 11-2003 | Konetski, David | 710/303 |
| | E | US- | | | |
| | F | US- | | | |
| | G | US- | | | |
| | H | US- | | | |
| | I | US- | | | |
| | J | US- | | | |
| | K | US- | | | |
| | L | US- | | | |
| | M | US- | | | |

FOREIGN PATENT DOCUMENTS

| * | | Document Number Country Code-Number-Kind Code | Date MM-YYYY | Country | Name | Classification |
|---|---|--|-----------------|---------|------|----------------|
| | N | | | | | |
| | O | | | | | |
| | P | | | | | |
| | Q | | | | | |
| | R | | | | | |
| | S | | | | | |
| | T | | | | | |

NON-PATENT DOCUMENTS

| * | | Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages) |
|---|---|--|
| | U | Creating a PCI Express™ Interconnect - Ajay V. Bhatt, Technology and Research Labs, Intel Corporation - 2002 |
| | V | PCI Express™ Base Specification Revision 1.0a - Section 1.1 - 15 April 2003 - PCI-SIG |
| | W | |
| | X | |

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.